

A blurred background image of a printed circuit board (PCB) with various components and traces.

Do you look for effective integration
of processor and FPGA in your
embedded application?

RSOC
FRAMEWORK

Two small squares, one blue and one black, are positioned above the section header.

Features

- interconnection of FPGA and processor
- rapid prototyping of applications
- standardized and easy-to-use interface
- customizable and platform independent
- effective drivers with automatic discovery
- scalable transfer speeds
- integrated with leading development tools
- fully tested and verified environment
- saves time for real development